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Application No. (if known): 09/894,203

Attorney Docket No. 299002053200

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Transmittal (1 page) Request for Reconsideration (7 pages)

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U.S. Pat and Trademark Office: U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Application Number 09/894.203 Filing Da:e TRANSMITTAL June 28, 2001 First Named Inventor Ichiro TOMOH:RO **FORM** Art Unit 2136 Examine: Name D. G. Cervetti (to be used for all correspondence after initial filing) Attorney Docket Number 299002053200 Total Number of Pages in This Submission ENCLOSURES (Check all that apply) After Allowance Communication Fee Transmittal Form Drawing(s) Appeal Communication to Board of Licensing-related Papers Fee Attached Appeals and Interferences Appeal Communication to TC x | Amendment/Reply (Appeal Notice, Brief, Reply Brief) Petition to Convert to a Proprietary Information x After Final Provisional Application Power of Attorney, Revocation Status Letter Affidavits/declaration(s) Change of Corresponder ce Address Other Enclosure(s) (please Terminal Disclaimer Extension of Time Request Identify below): Request for Refund **Express Abandonment Request** CD, Number of CD(s) Information Disclosure Statement Certified Copy of Priority Landscape Table on CD Document(s) Reply to Missing Parts/ Remarks Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name **MORRISON & FOERSTER LLP** Signature ahr Printed name Adam Keser Reg. No. Date 54,217 February 26, 2007

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Docket No. 299002053200

(PATENT)

Client Reference No.: F5-01965232/01R00118-1US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Ichiro TOMOHIRO

Application No. 09/894,203

Confirmation No. 7078

Filed: June 28, 2001

Art Unit: 2136

For: SEMICONDUCTOR STORAGE DEVICE

Examiner: David G. Cervetti

REQUEST FOR RECONSIDERATION

MS AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the final Office Action dated October 24, 2006, and further in response to the Advisory Action mailed February 12, 2007, please reconsider this application in light of the following remarks.

REMARKS

Claims 1-10 stand rejected under 35 USC 102(b) on Vicard (U.S. Patent No. 5,708,715). Applicant respectfully traverses this rejection.

Applicant previously argued that Vicard discloses a semiconductor memory incorporating a conventional data tamper prevention circuit similar to that depicted in applicant's Figure 3, and therefore does not disclose or suggests "at least one memory region, each one of said at least one memory region being provided in the at least one memory cell array block, for storing the security release key; [and] at least one non-volatile storage mechas for storing a security registration lock"

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